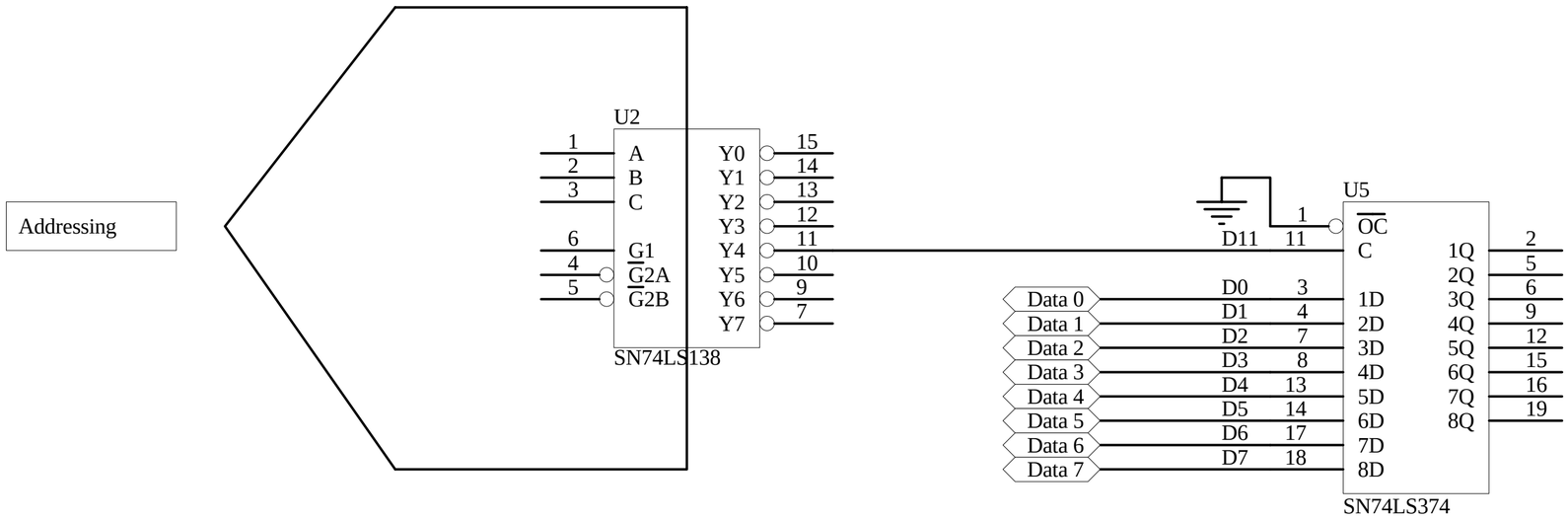

Title *Output Circuit*			
Size: A	Number:	Revision:	*Dan Kohn* *NC A&T State University*
Date: 27-Mar-2008 Time: 10:33:29		Sheet0 of 0	Protel Making Electronic Design Easy™
File: C:\Documents and Settings\dekohn\My Documents\New folder\Lab_Project.ddb - Documents\Output.Sch			