ECT 213
Spring 2008
Lab #6
Exclusive OR - Parity Checking

Name:_____

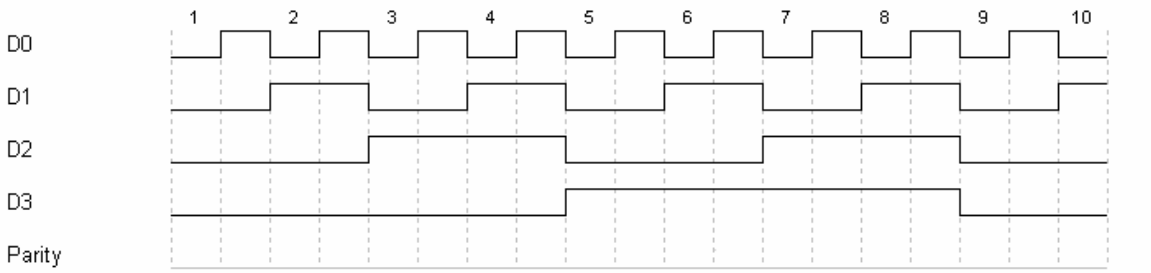
This is due at the end of class. Turn in your Hades schematics and hand written lab sheet for credit.

Using HADES, draw the circuit for a 4-bit **<u>even</u>** parity generator as discussed in class. Mark the schematic with the pins that will be used on the IC. Make sure to indicate the IC(s) used.

What is the equation for the circuit?

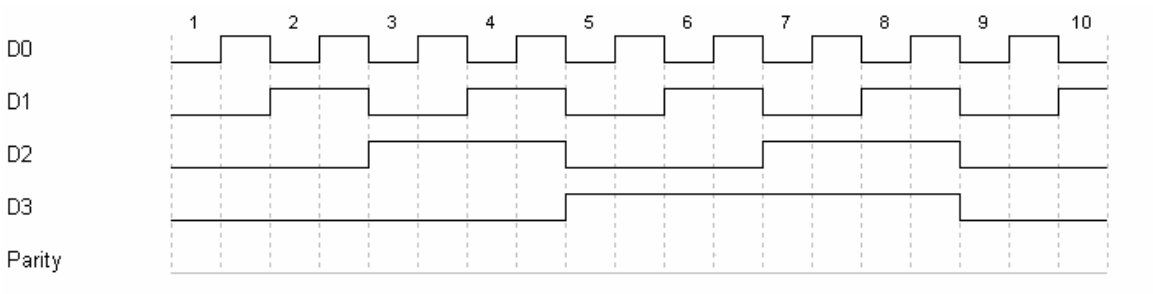
Build the circuit and demonstrate it to the instructor. (Make sure he initials your drawing).

Complete the following Timing Diagram:



Now, draw the circuit for a 4-bit **odd** parity generator as discussed in class. Mark the schematic with the pins that will be used on the IC. Make sure to indicate the IC(s) used.

What is the equation for this circuit?

Build the circuit and demonstrate it to the instructor. (Make sure he initials your drawing).

Complete the following Timing Diagram: